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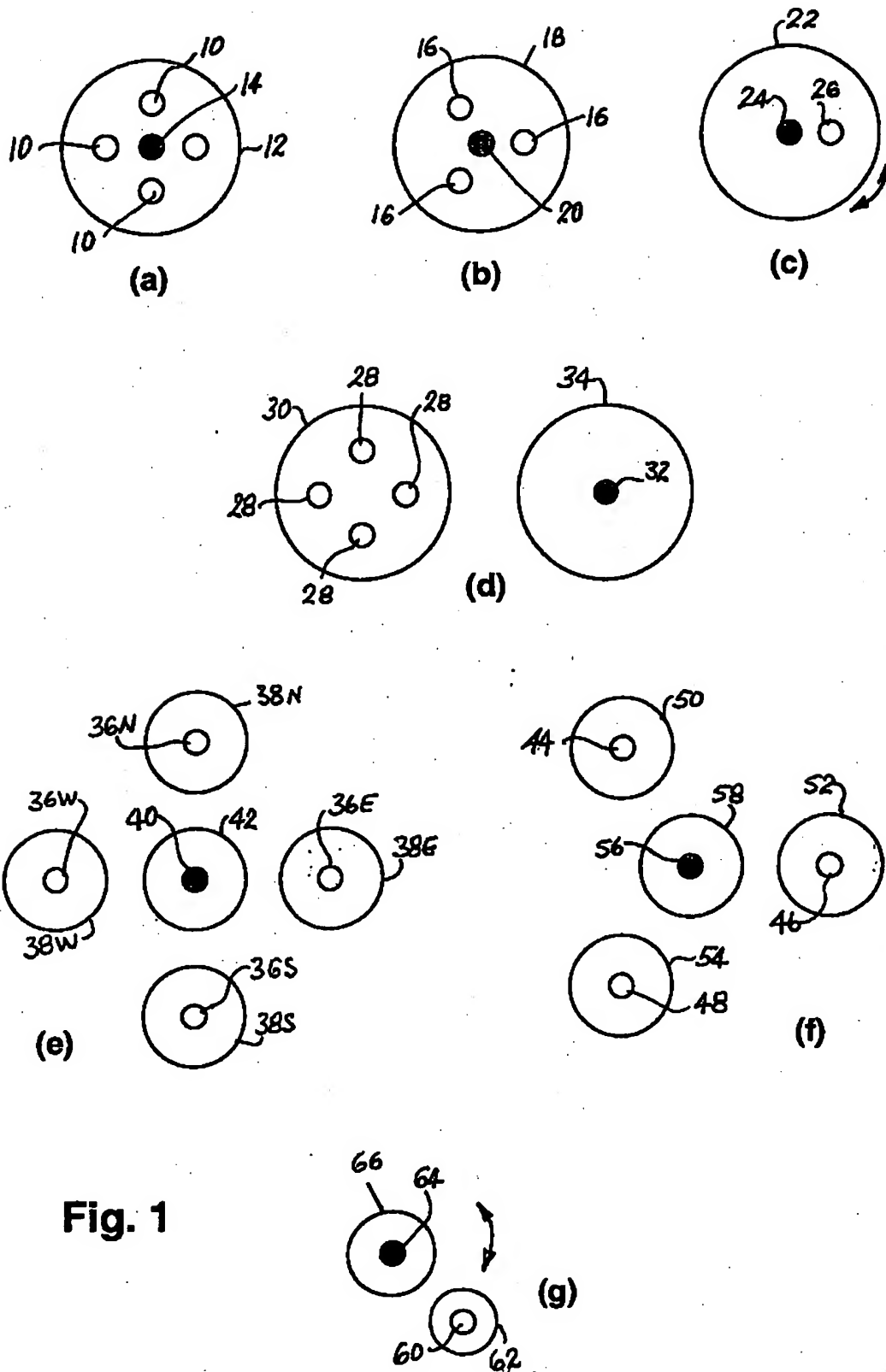


Fig. 1

Fig. 1

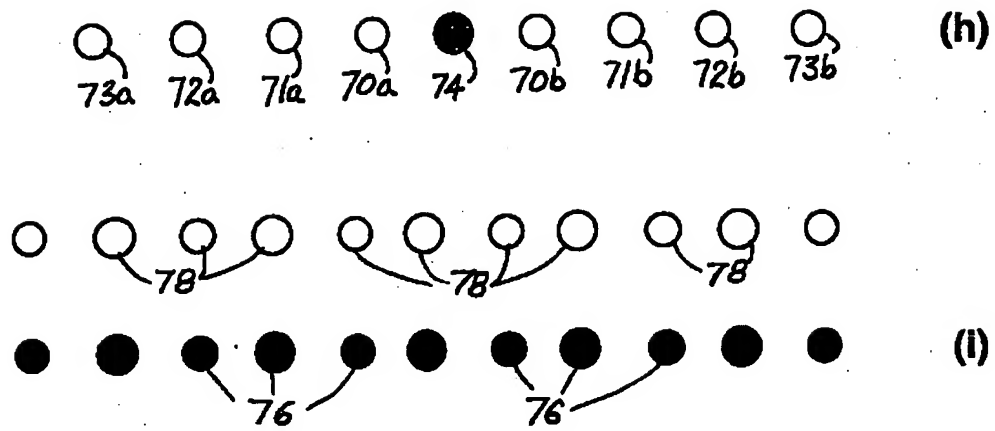
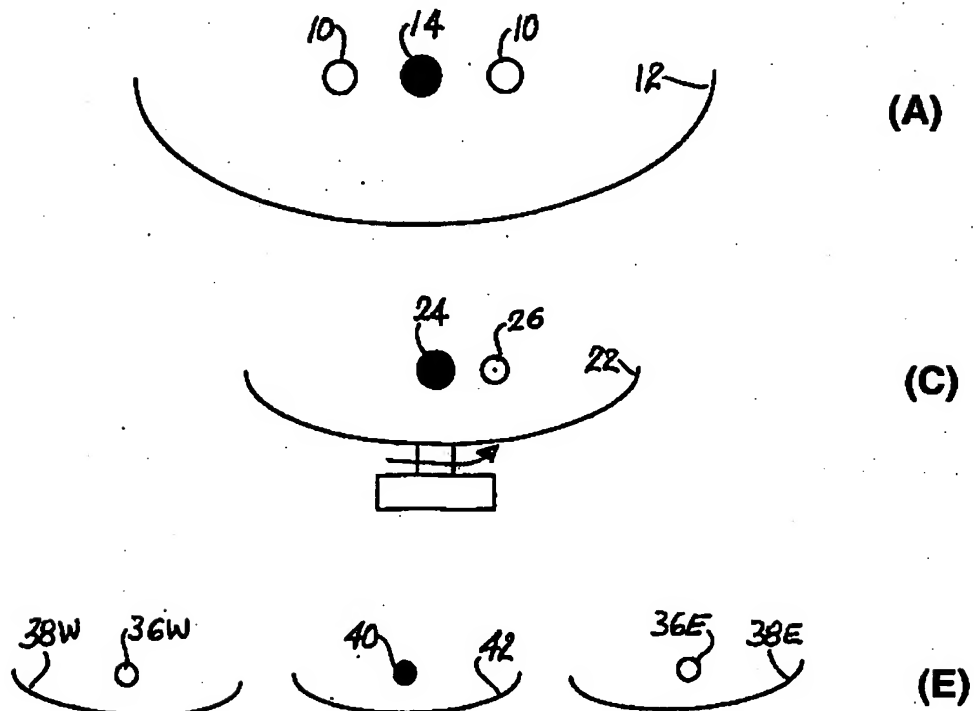
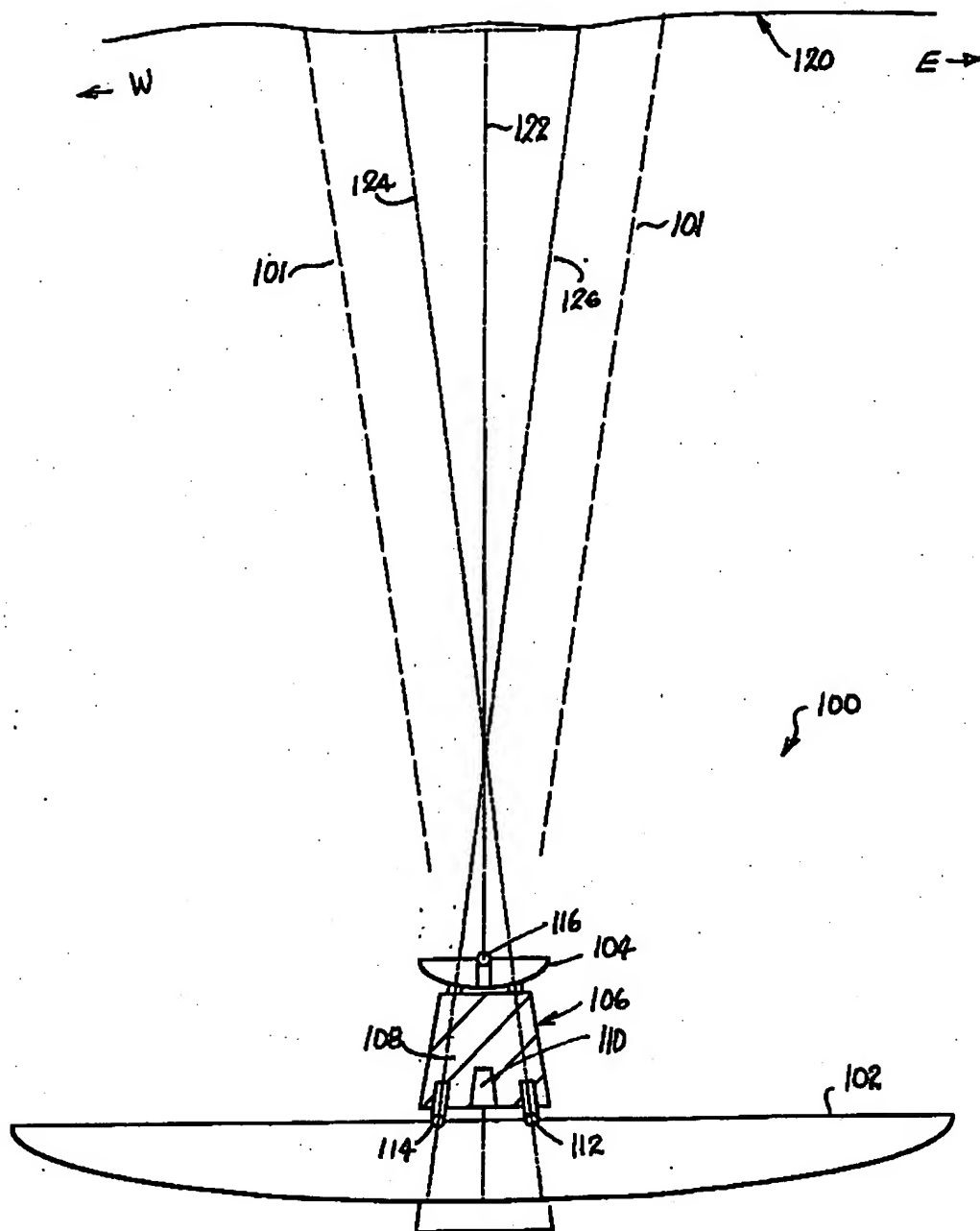


Fig. 2



**Fig. 3**

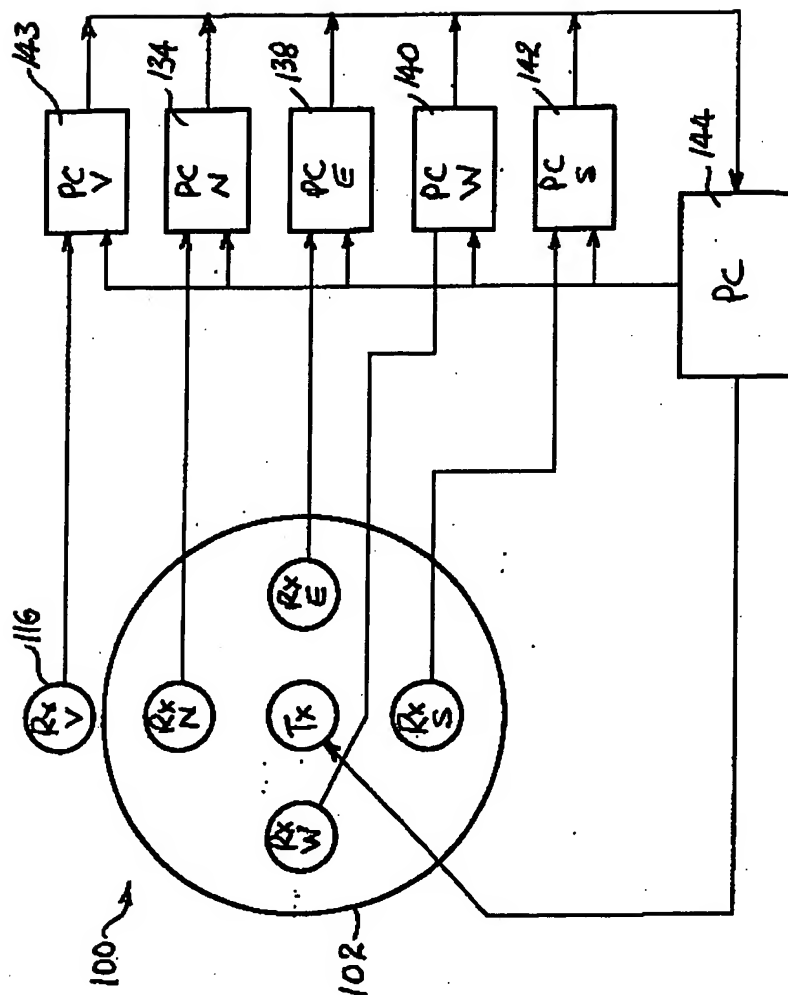


Fig. 4

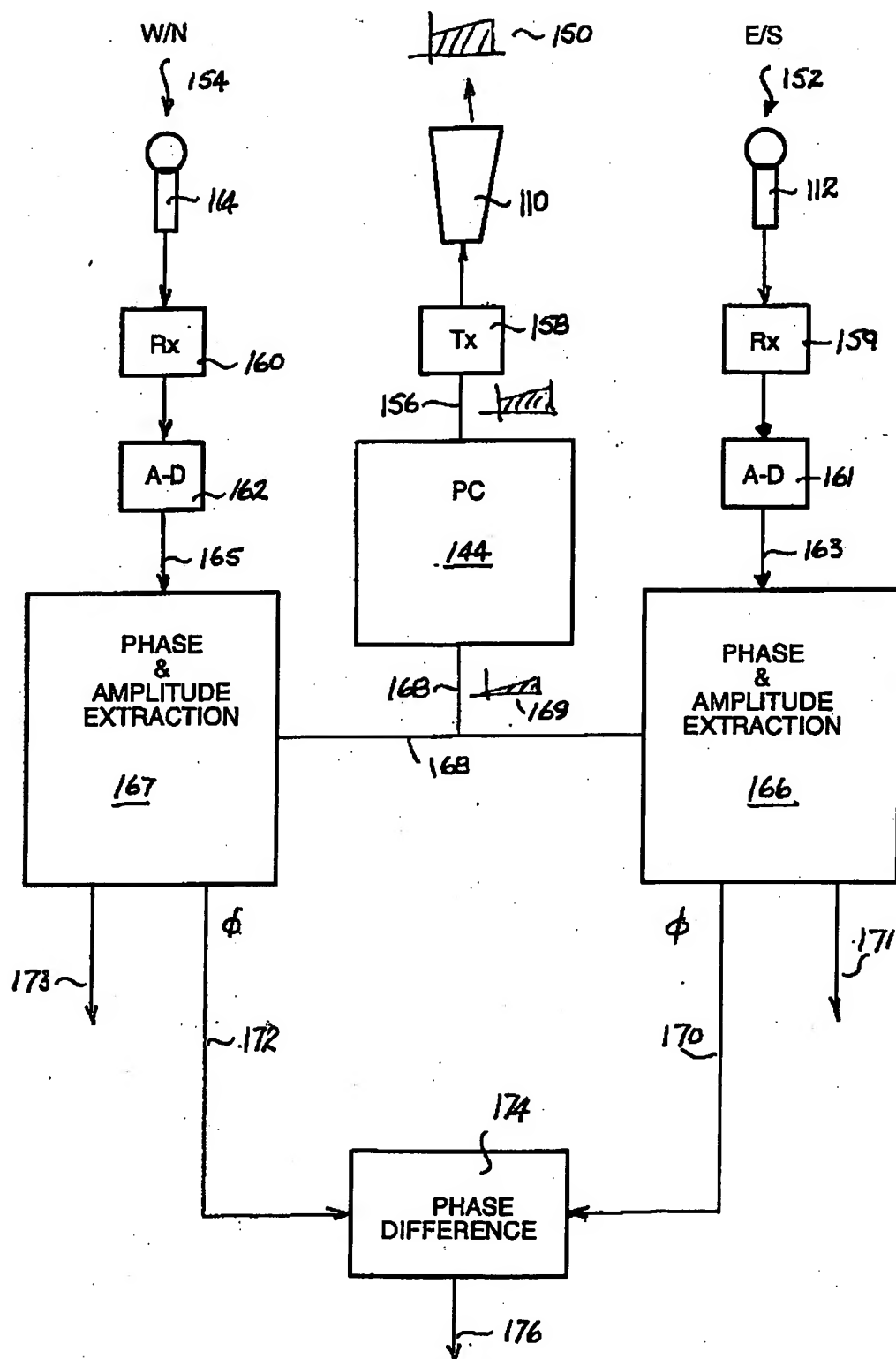
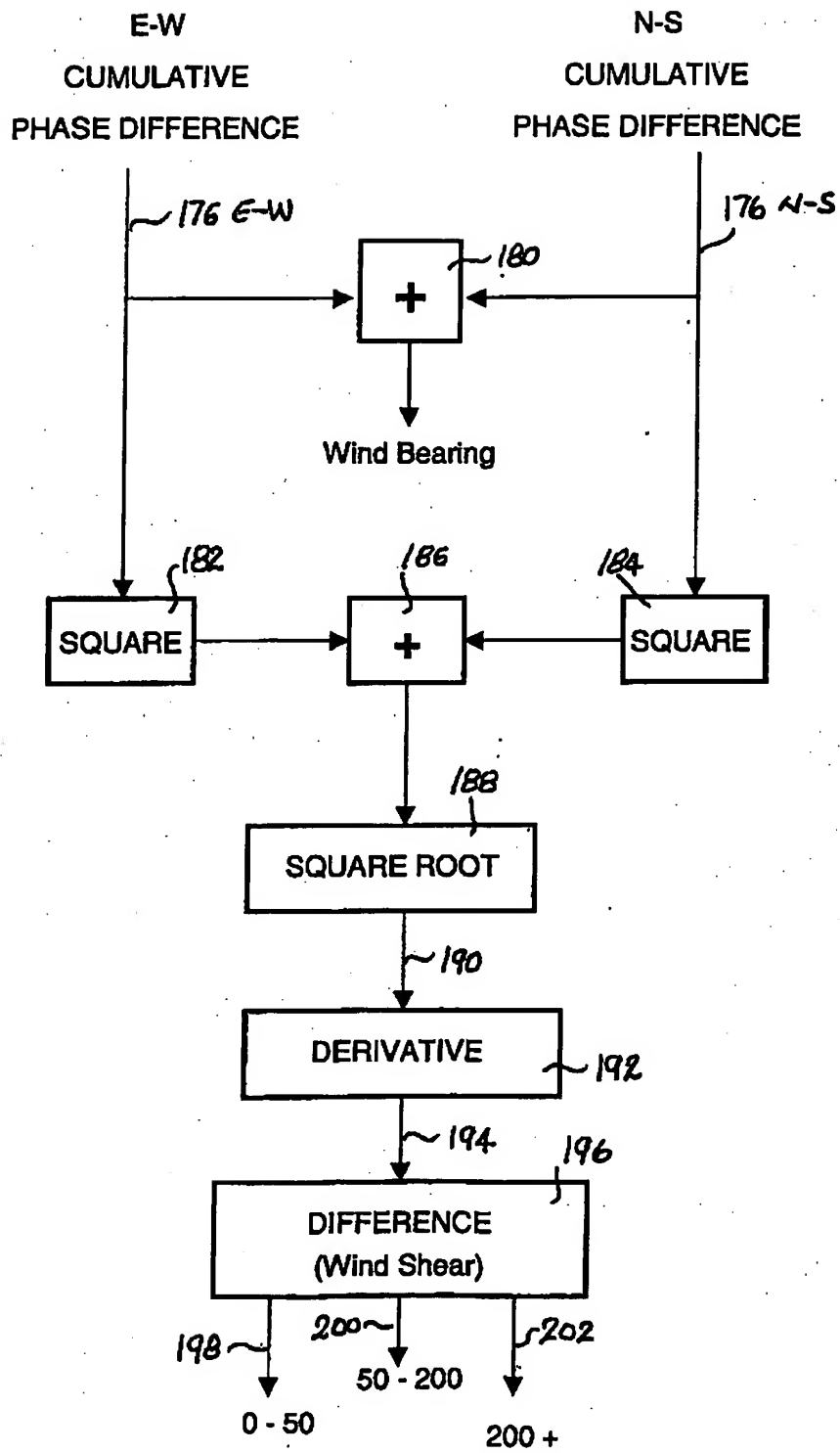


Fig. 5A

**Fig. 5B**

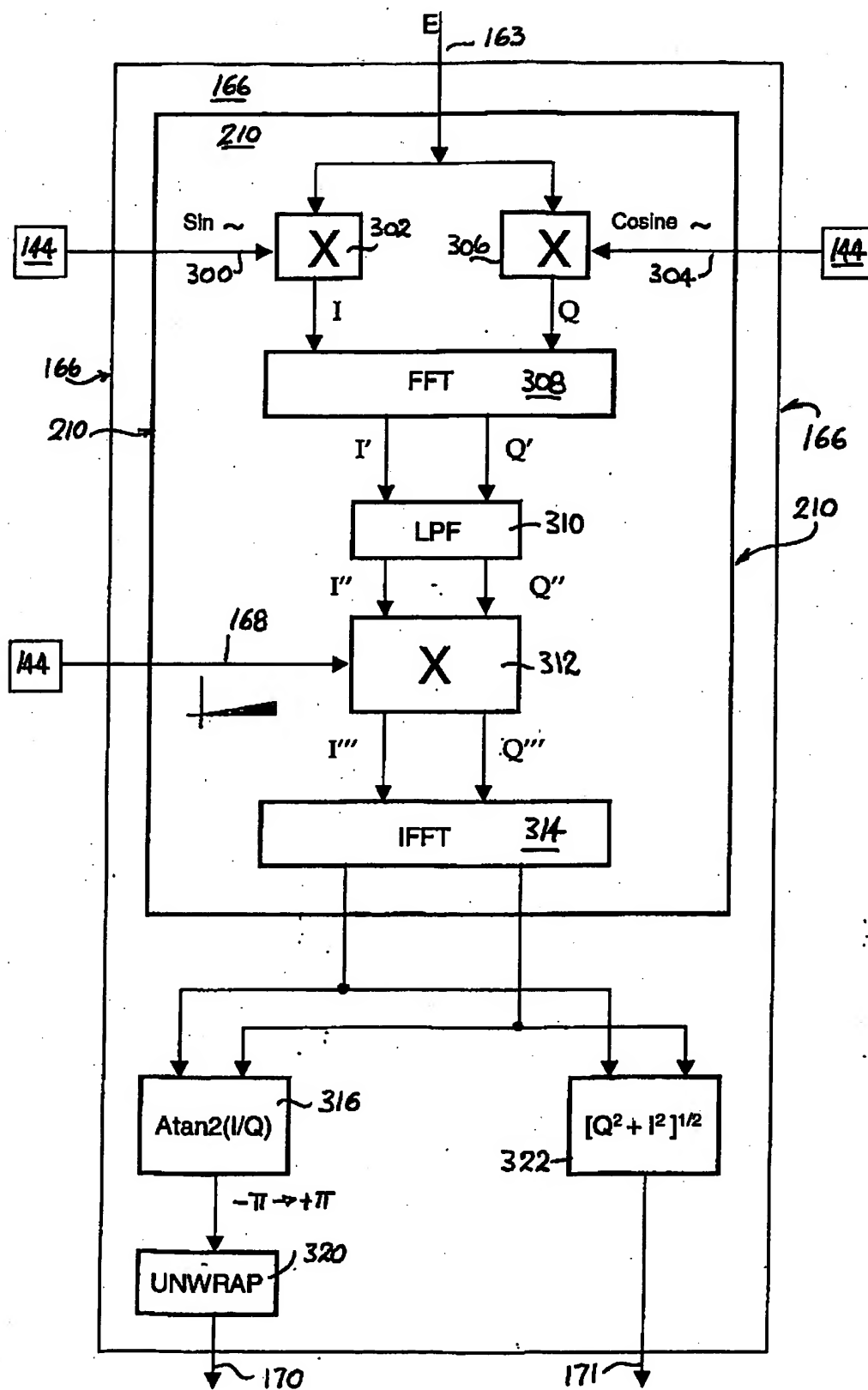


Fig. 5C

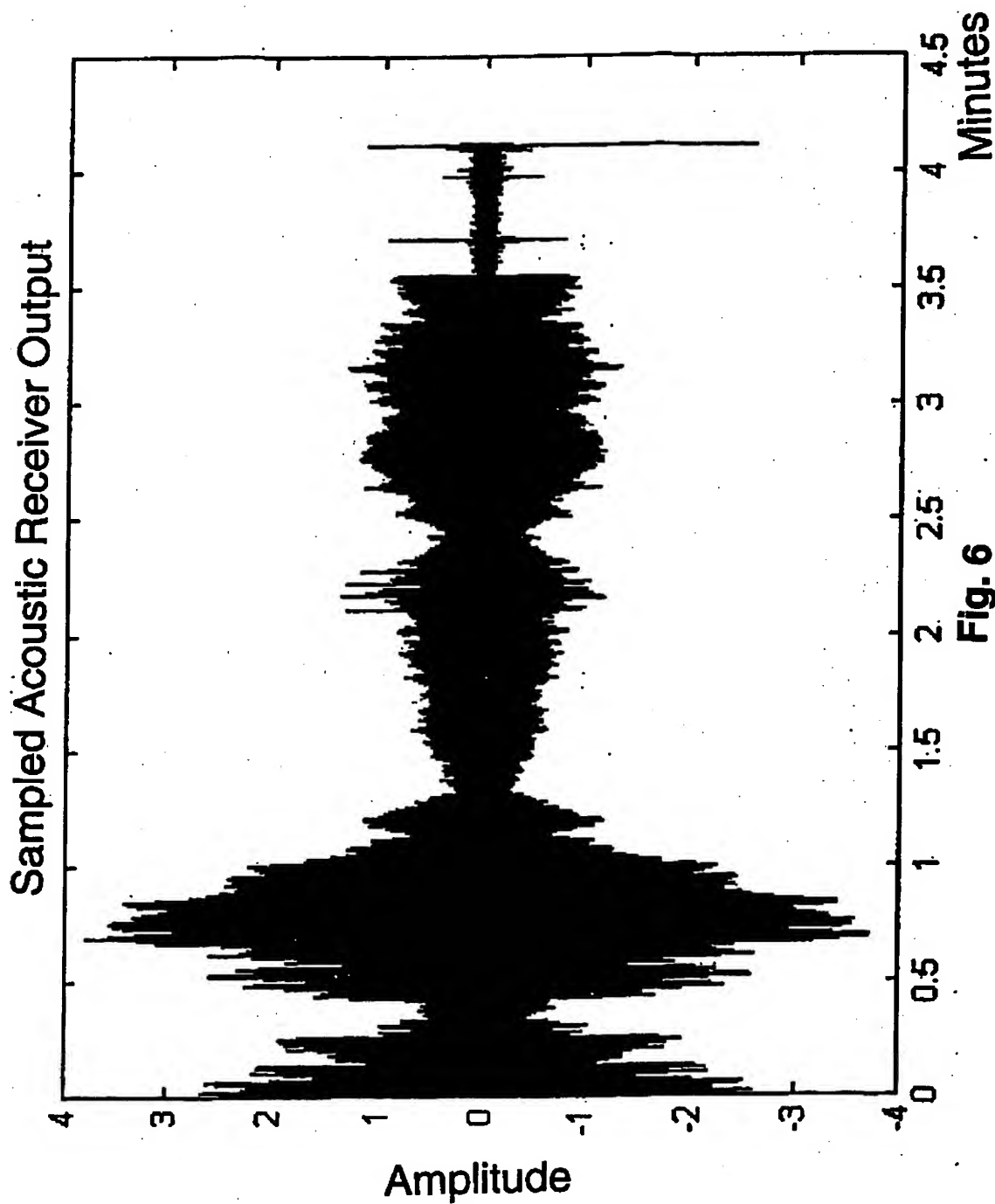
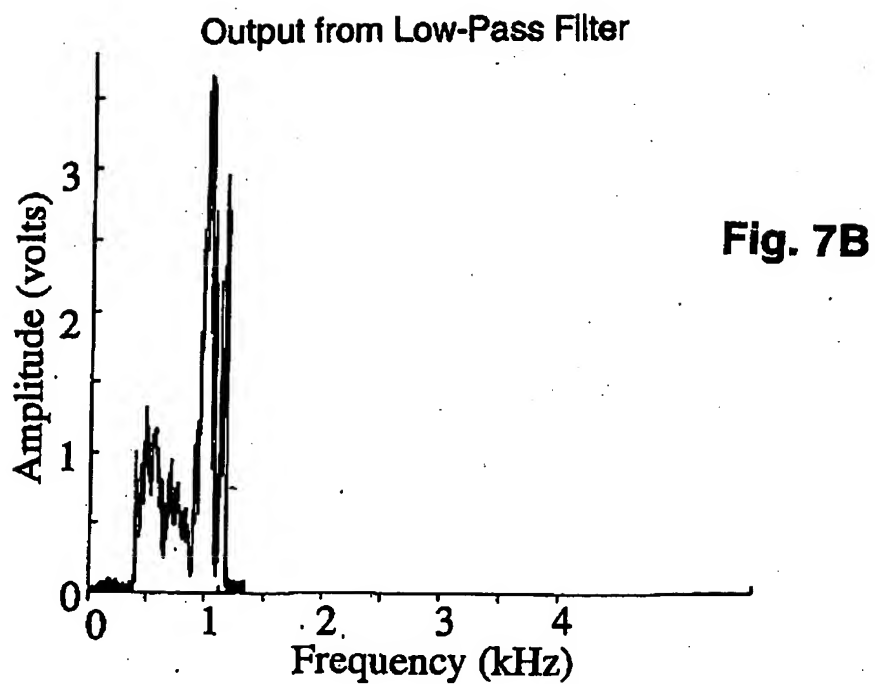
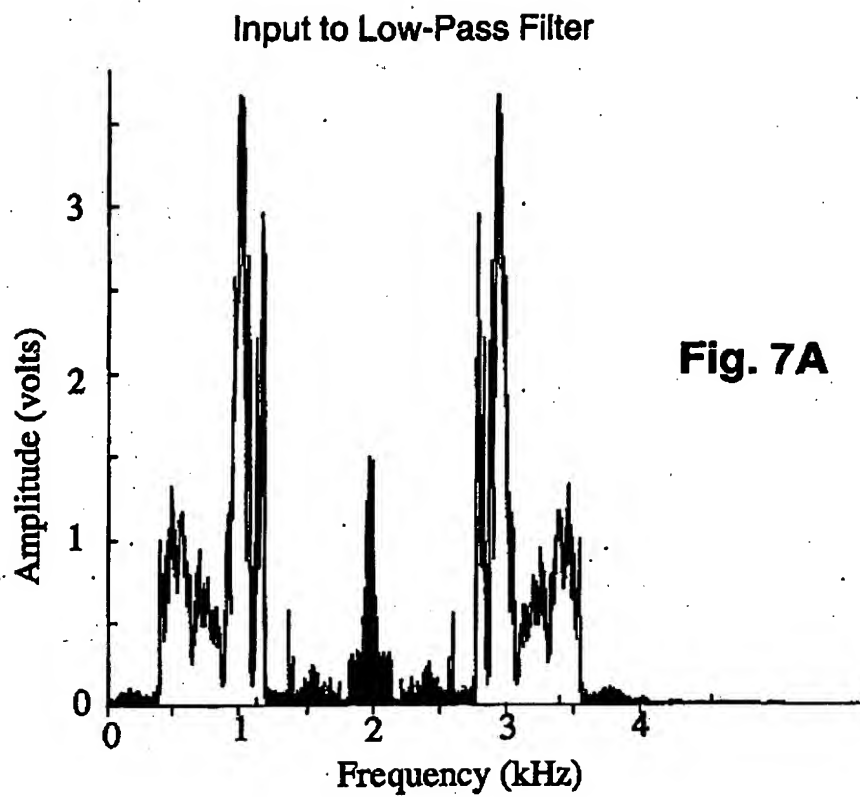


Fig. 6



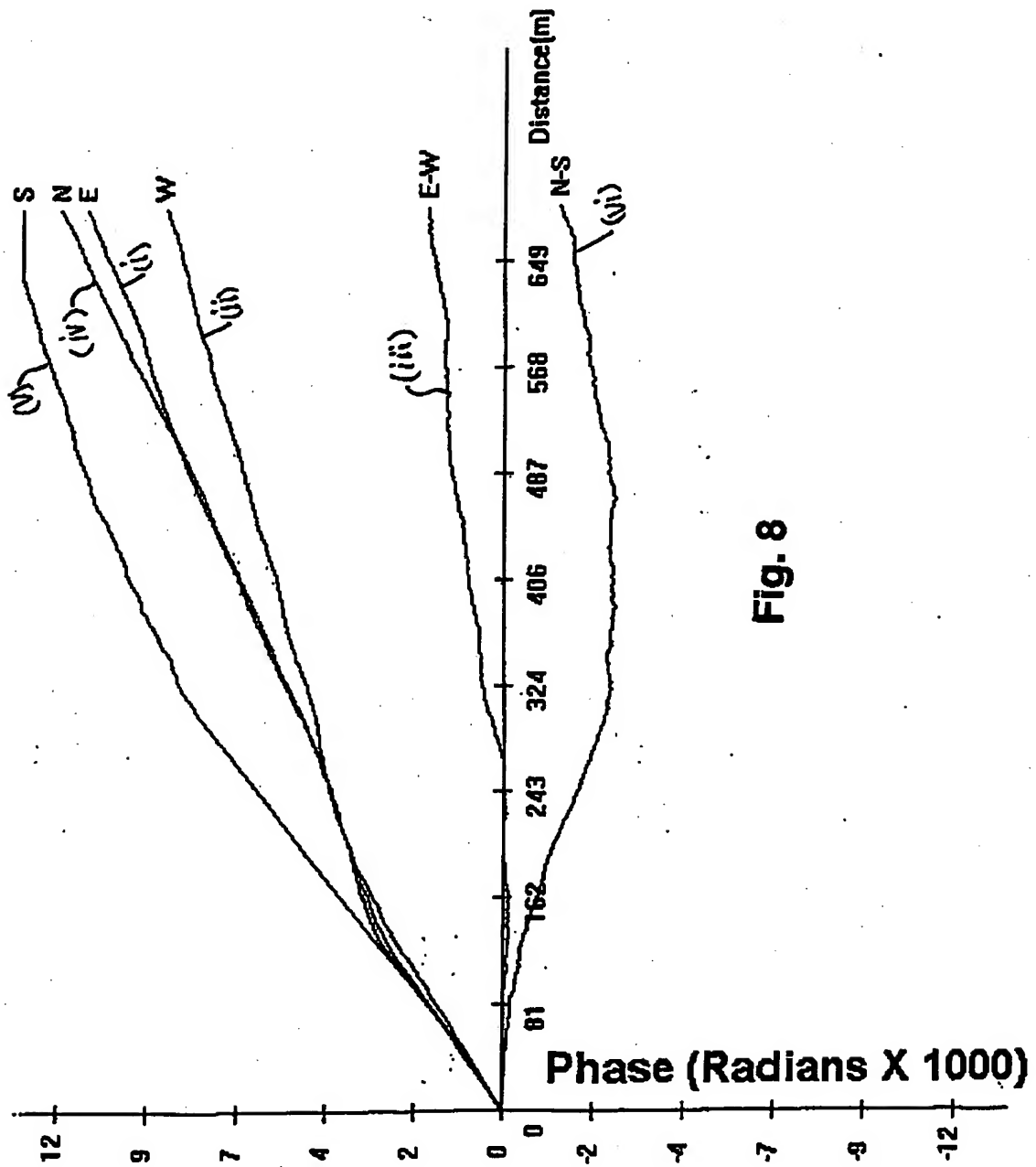
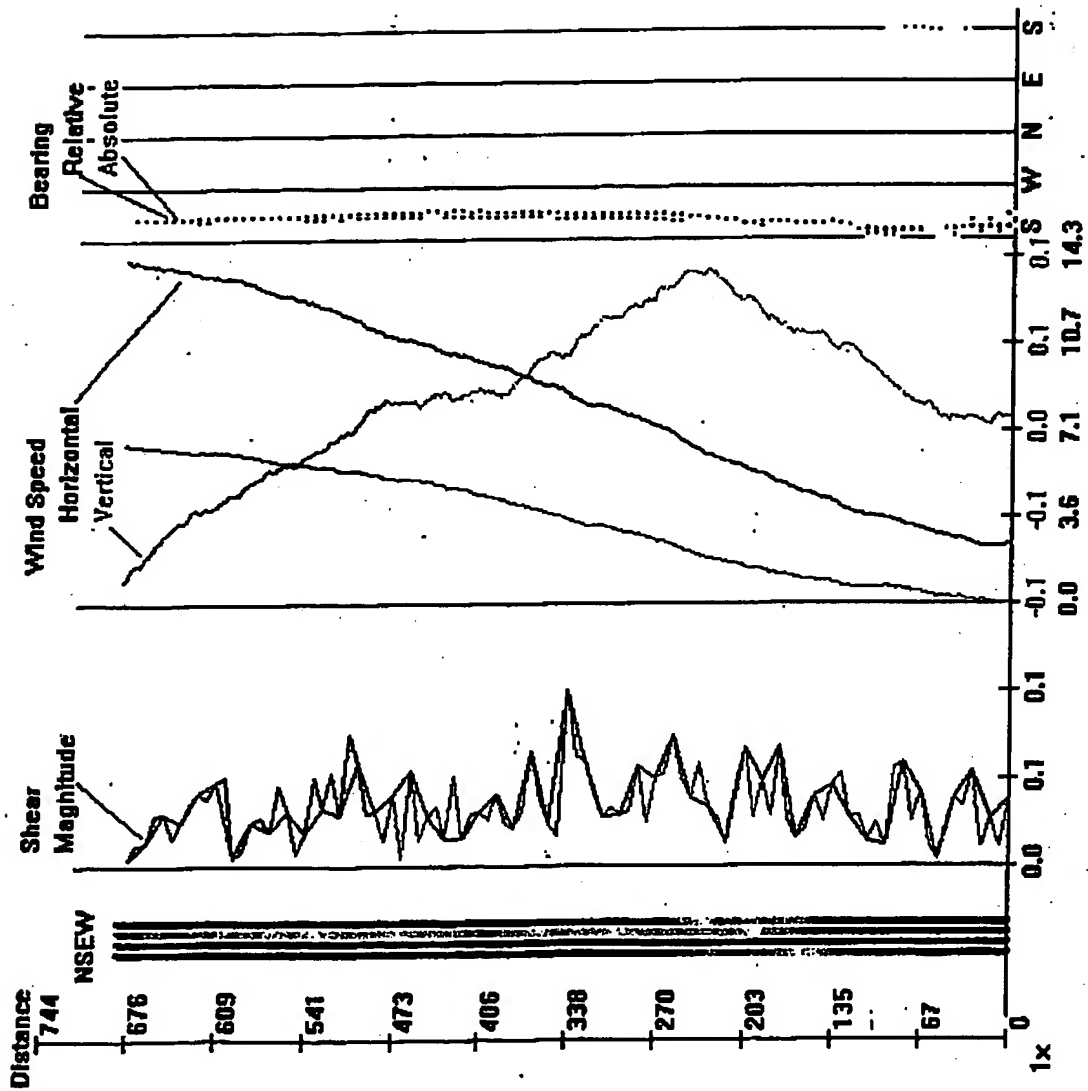


Fig. 8

Fig. 9



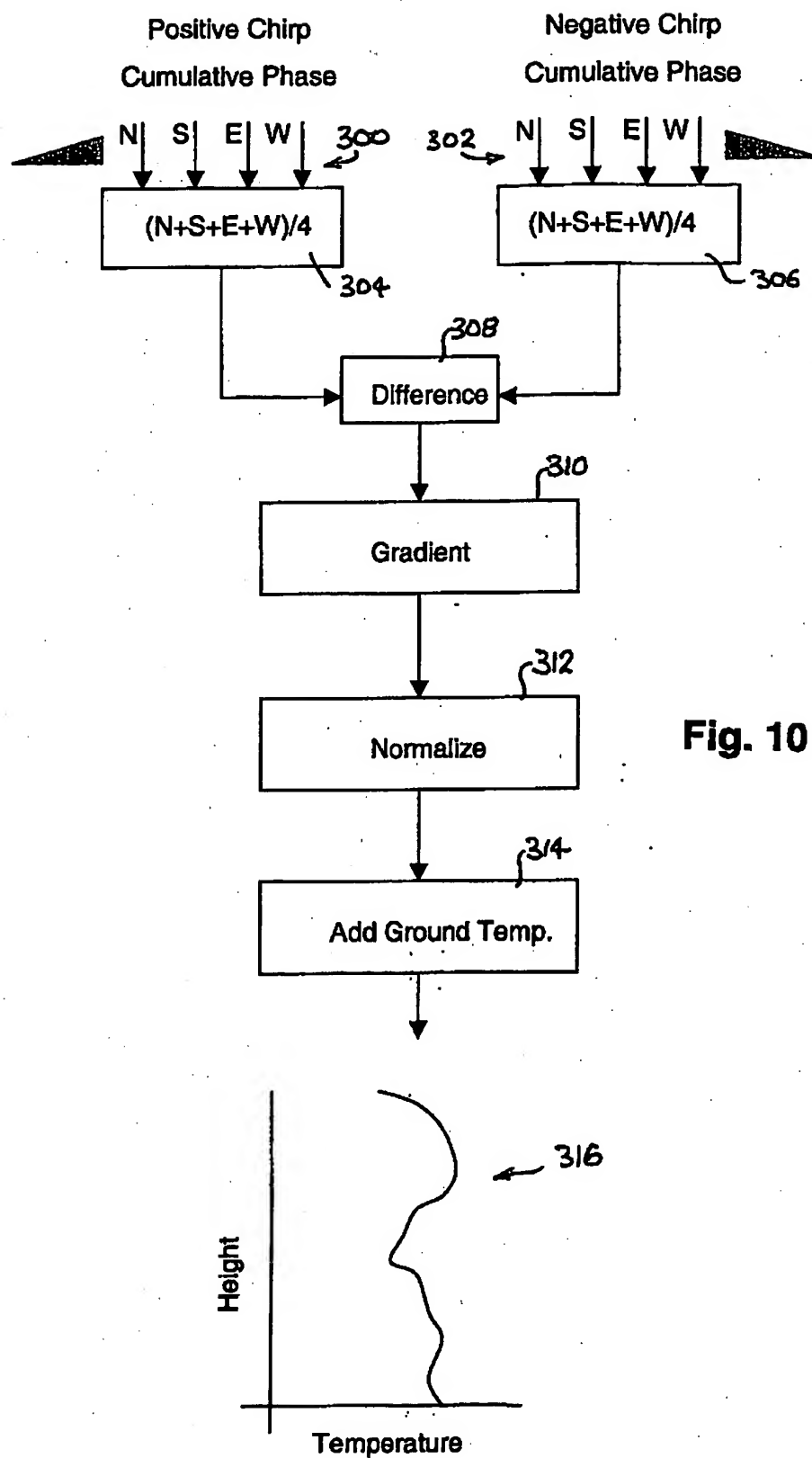


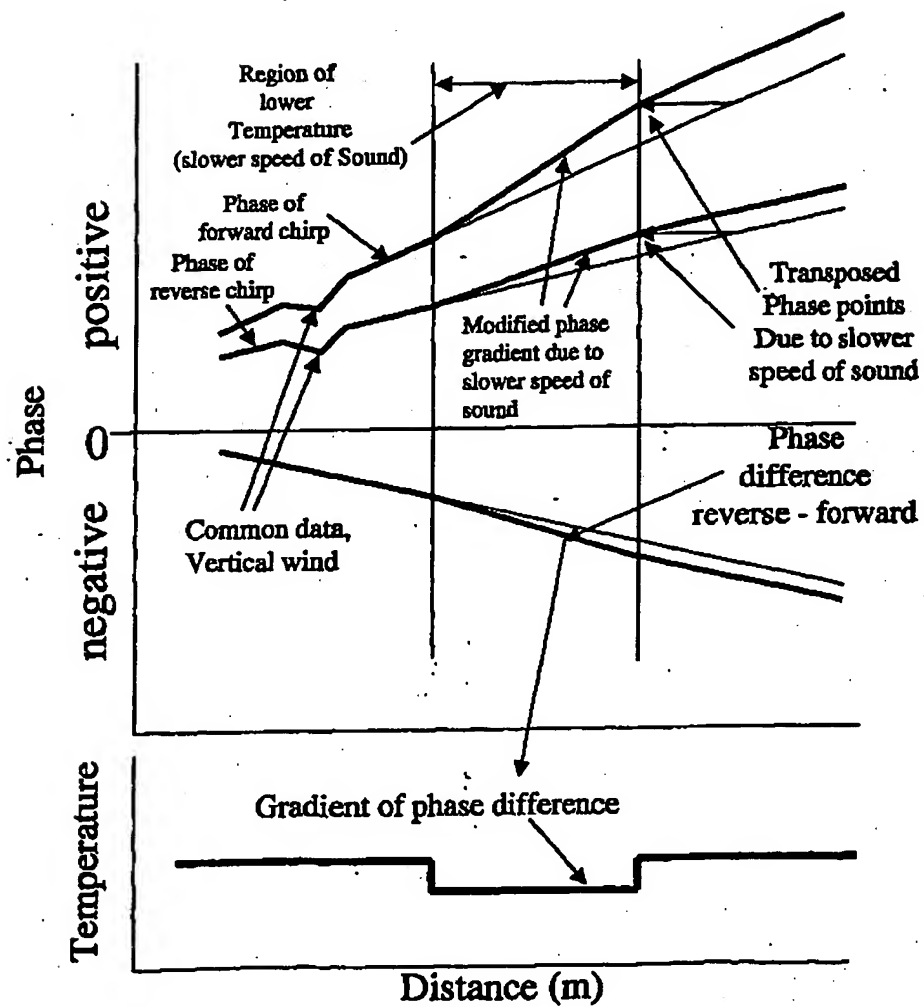
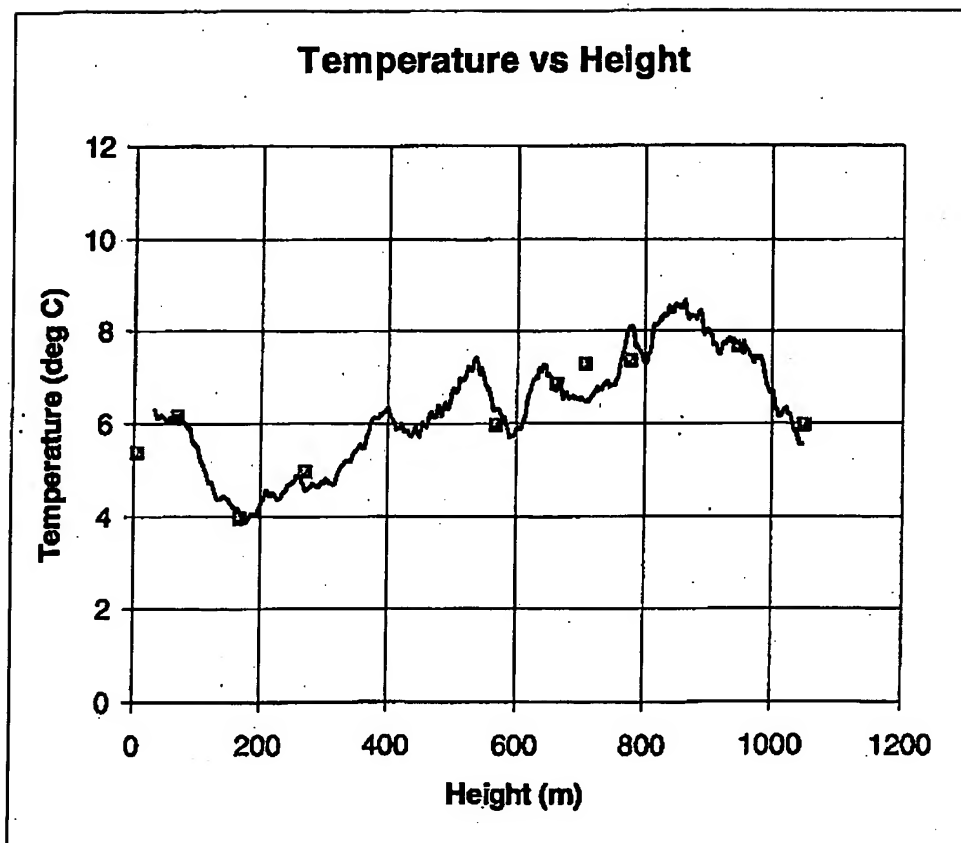
Fig. 11**Derivation of Temperature From Phase Inputs**

Fig 12

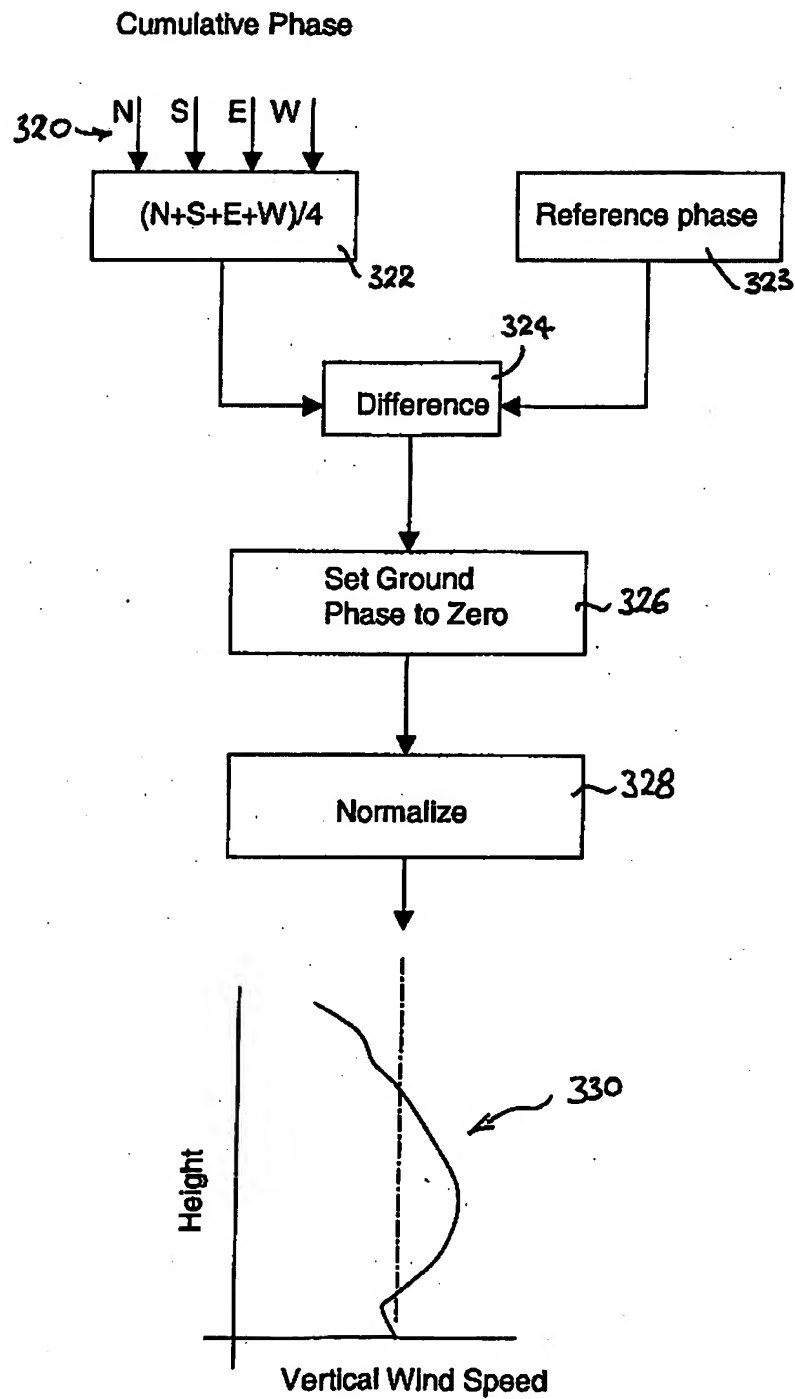
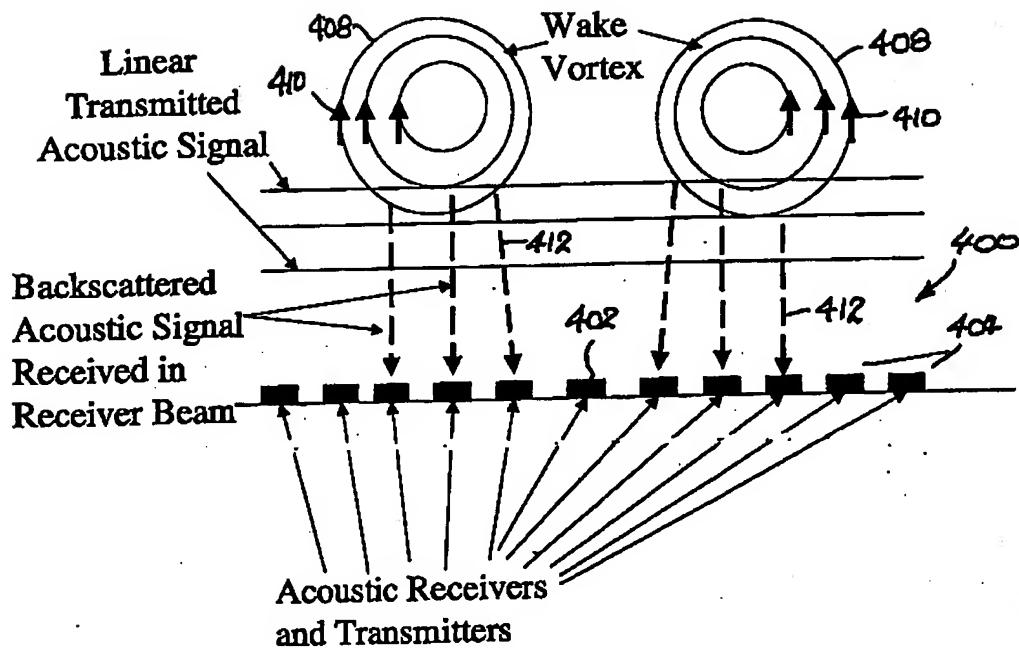


Fig 13

Fig. 14**Fig. 15**